

CUSTOMER NUMBER 27792

INFORMATION DISCLOSURE STATEMENT LISTING SHEET

Information Cited By Applicant(s) That May Be Material To
The Prosecution Of The Subject Application

Applicant:

Michael P. Foley

Attorney Docket No. MICR0456

Serial No.:

10/697,420

Group Art Unit: 2171

Filed:

October 30, 2003

Examiner:

Title:

REORDERING DATA BETWEEN A FIRST PREDEFINED ORDER AND A SECOND PREDEFINED ORDER WITH SECONDARY HARDWARE

U.S. PATENT DOCUMENTS

NONE CITED

FOREIGN PATENT DOCUMENTS

NONE CITED

OTHER INFORMATION

Examiner	Document	
Initial	<u>No.</u>	Document Information
JH	01	International Business Machines Corporation. 2/21/2000. PowerPC TM Microprocessor Family: The Programming Environments for 32-Bid
		Microprocessors. "Chapter 3: Operand Conventions" 14pp.

/Joni Hsu/	09/07/2006
Examiner's Signature	Date

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

TRM/RMA:ssa 2/3/04